



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

7/B
J. BELL
2.4.03

In re Application of:

Noriaki HIRAGA

Application No.: 09/625,643

Filed: July 25, 2000

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE WITH
ENHANCED RESISTANCE TO
ELECTROSTATIC BREAKDOWN

Confirmation No.: 7356

Group Art Unit: 2836

Examiner: Z. Kitov

RECEIVED
JAN 31 2003
TECHNOLOGY CENTER 2800

Commissioner for Patents
Washington, D.C. 20231

Sir:

**AMENDMENT UNDER 37 C.F.R. § 1.111 AND
REQUEST FOR RECONSIDERATION**

In response to the Office Action dated November 4, 2002 (Paper No. 6), the period for response to which extends through February 4, 2003, please amend the above identified application as follows:

IN THE ABSTRACT:

Please amend the abstract as follows:

12/ A semiconductor integrated circuit device having a plurality of internal circuits connected to different power lines, and an inter-circuit signal wire or a branched wire along these internal circuits, wherein near an active element in a first connection configuration connected to the inter-circuit signal wire or the like, a plurality of active elements in a second connection configuration are arranged to sandwich or surround the active element in the first connection configuration.